

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	107	712/19	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/16 11:07
L2	1	(716/1).ccls. and (processing adj circuit) and (parallel adj process\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/16 11:10
L3	1	(716/2).ccls. and (processing adj circuit) and (parallel adj process\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/16 11:09
L4	15	("716"/\$).ccls. and (processing adj circuit) and (parallel adj process\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/16 11:10
L5	1	(712/19).ccls. and (processing adj circuit) and (parallel adj process\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/16 11:11
L6	1588	(processing adj circuit) and (parallel adj process\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/16 11:11
L7	7	(processing adj circuit) and (parallel adj process\$5) and (linear adj transform\$7)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/16 11:14
L8	15	(processing adj circuit) and (first same second same linear adj transform\$7)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/16 11:14
L9	15	(processing adj circuit) and (first same second same linear adj transform\$7) and (first same second samd processing\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/16 11:15

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L10	10	(processing adj circuit) and (first same second same linear adj transform\$7) and (first same second same processing\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/16 11:15
L11	1	((processing adj circuit) and (first same second same linear adj transform\$7) and (first same second same processing\$2)).CLM.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/16 11:16
S1	1961	716/1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/16 11:07
S2	1692	716/2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/16 10:55